


PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>  </p> <p> O.I.P.E. </p> <p> SCANNED </p> <p> Q.A. </p>	<p> PATENT DATE </p>
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APPLICATION NO. 09/922384	CONT/PRIOR D F	CLASS 029	SUBCLASS	ART UNIT 3729	EXAMINER <i>catb50H</i>
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APPLICANTS

Dai Sasaki
Tohru Terasaki
Masuo Kato
Masami Tsurumi

TITLE

Mounting structure of a semiconductor device, and a process for mounting a semiconductor device on a mounting substrate

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____ _____	
	_____ (Primary Examiner)		ISSUE FEE	
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